

WiBORNE, INC.

MULTIPLEXER SOLUTION SERIES

OMNIMUX: 8/16 E1/T1 OVER IP

Evolutionary Convergence in Transmission of Traditional Voice/Video and Data over Ethernet Network

PRODUCT OVERVIEW

OmniMux is designed as a multi-service access platform for PDH over IP applications. E1 frames can be mapped/de-mapped into/from IP packets. An adaptive clock recovery method for Ingress PDH (PSN -> TDM) clock generation is implemented to support E1 (ITU-T G.823) Jitter performance.

COST-EFFECTIVE IP DEPLOYMENT (PDH OVER IP)

OmniMux provides cost-effective applications of traditional circuit-switched system over IP. With OmniMux, it is easy to interconnect existing phone systems over IP that are used to carry data, voice and video.

2G/3G/4G BACKHAUL DEPLOYMENT

With high precision clock recovery technology, OmniMux is capable of supporting 2G/3G/4G backhaul and provides smooth services.

TRANSPARENT TRANSMISSION

OmniMux can transparently transport proprietary signaling that are required to support PBX features, including call conference, call forwarding and SS7. Customer can easily apply and enjoy better integration of TDM and IP devices with lower network expense.

BYPASS INTERNATIONAL TOLL

With a pair of OmniMuxs and guaranteed internet bandwidth, it is sure to save cost dramatically, and to ensure the QoS of voice based on interconnections of TDM equipment.



FEATURES

- Support IETF RFC4553 Structure-Agnostic TDM over Packet (SAToP), Metro Ethernet Forum MEF8.
- Two PSN(GE) interface with 1 + 1 redundant protection .
- 8 /16 x E1 NRZ Serial Interface with LOS/AIS detection
- Use Raw Encapsulation method for PDH payload over IP packet.
- Support Circuit Emulation Service over IP.
- Comply with IETF draft standard for CESoPSN and SAToP; Metro Ethernet Forum MEF8 IA.
- Support both Point-to-Point and Point-to-Multipoint operation.
- Support 8/16 independent Adaptive Clock recovery block for Ingress PDH (PSN -> TDM) clock generation. Recovered clock jitter is compliant with ITU-T G.823 (E1 Jitter Control).
- Independent configurable jitter buffer depth to compensate up to 250ms of Packet Delay Variation.
- Lost packets processing/compensation via PW (Pseudo Wire) control field Sequence Number.
- PDH LOS detection triggered PW L field or payload AIS generation at Egress direction (TDM -> PSN).
- Configurable IEEE 802.3 DA/SA assignment.
- LED alarm display for E1/Power failure

SPECIFICATION

LINE INTERFACE

Port: 8/16 x E1 (ITU-T G.703)
Interface: RJ-48c (120 Ohm)
Line Coding: HDB3

OPTICAL ETHERNET INTERFACE

WAN Port: 2 x 1000Base-X SFP slot
Connector: SFP module cage
Rate: up to 1Gbps

DIMENSIONS

H x W x D: 44 x 290 x 267.6 (mm)

MAIN POWER SUPPLY

AC: 85 ~ 264V @ 47 ~ 63Hz
(Optional) DC: -72V ~ -36V

ENVIRONMENT CONDITION

Ambient temperature: 0°C ~ 50°C (0°C ~ 65°C, optional)

Storage temperature: 0°C~ 85°C

Relative humidity: 5 ~ 95% non condensing

CONFIGURATION AND MANAGEMENT

RS-232 console port (Craft Terminal) or Telnet/SNMP-based management via NMS port.

ORDERING INFORMATION

[**Line Interface**] 8x E1 16x E1 8x T1(Call) 16x T1(Call)

[**WAN Interface**] 2x 1000Base-X SFP slot for fiber link

[**Management**] Craft terminal Craft terminal + SNMP-Based Management

[**Power**] 1x AC 1x DC 1x AC+ 1x DC

OMNIMUX APPLICATION

